



C E R T I F I C A T E

Certificate registration number: G3.1606.087.1.B

Certificate holder: ATMEL Spain S.A.U.

Platform designation: ATPL250A,
Hardware version motherboard: ATPL250AMB v1,
daughterboard: ATPLCOUP002 v2,
Firmware version G3_v1.2.2

Certification date: June 28th, 2016

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (05-2013) plus the changes listed in the annex to this certificate.

The certificate applies to certification profile Japan Route B and the device was configured as a PAN-Device.

Test cases have been performed as described in the test reports referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in April 2016. The results and remarks can be found in the complete test reports.

Applied tests	Performed by	Document evidence
Conformance and interoperability testing according to the test specification referenced by the test report	Laboratoire des Applications Numériques (LAN)	Test report LAN16AF039

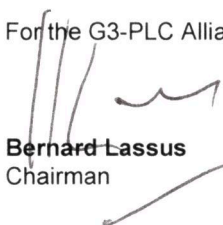
The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. This certificate is valid from June 28th, 2016.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3- PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, June 28th, 2016

For the G3-PLC Alliance:


Bernard Lassus
Chairman


Madeleine Francillard
Chair Certification Program





Annex 1: Reference Version for Certification

The reference version for this certificate is:

Recommendation ITU-T G.9903 (05-2013)

- + CCTT_121: Correction of Beacon payload encoding
- + CCTT_122: Japan HEMS requirements for ID_S and ID_P
- + ETT_DLL30 and ETT_DLL32: Routing disabling
- + ETT_PHY14: Interleaver fix (conditional swapping)
- + CCTT_171: Implementation of MAC security solution F2

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Certificate registration number: G3.1606.087.1.B

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Annex 2: Protocol Implementation Conformance Statement (PICS)

Feature implementation statement

Name	Value	Description
BAND_PLAN	ARIB	Indicate the band-plan supported by the device.
FEATURE_PAN_COORDINATOR	FALSE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false)
FEATURE_COHERENT_MODULATION	FALSE	Indicate if coherent modulation is supported
FEATURE_EAP_SERVER	FALSE	Indicate if an EAP-PASK server is implemented by the DUT. Apply only if FEATURE_PAN_COORDINATOR=true
FEATURE_D8PSK_MODULATION	FALSE	True / False
FEATURE_ROUTING	FALSE	Indicate if the routing is implemented by the IUT
FEATURE_SECURITY	F2	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up
FEATURE_PREAMBLE_COEXISTENCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.

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Annex 3: Copy of test report cover sheet



G3-PLC Certification Test Report			
ATMEL	ATPL250A HW Mother B: ATPL250AMBv_1 Daughter B: ATPLCOUP002v_2 FW: G3_v1.2.2		
LAN16AF039	Ed.00	April 25, 2016	Page 1/22

G3-PLC Alliance
G3-PLC Platform Certification Test Report

Vendor Name **ATMEL**
 Model Name **ATPL250A**
 Serial N° **14090890089**
 HW version **Mother B: ATPL250AMBv_1 Daughter B: ATPLCOUP002v_2**
 FW version **G3_v1.2.2**

Test Report # **TR_LAN16AF039 Ed.00**
 Date **April 25, 2016**

CONF Tests Specification **version 0.19. 01/09/2015**
 CONF Tests Suite **version 2.1. 10/2015**
 IOT Tests Specification **version 0.7. 21/04/2015**
 IOT Tests Suite **version 2.1. 10/2015**

Test Tool **version 1.7**
 Tester Modem **version 1.09**
 Certification Test Procedures **version 1.6. 31/12/2015**

Certification Profile **ROUTE B (ARIB)**
 Role **PAN Device**
 Overall Verdict **PASS**



Initiation	Date	Description of modification	Ed.
Omar DIOUF	April 25, 2016	Creation	00

Name	Realised by	Checked by	Approved by
Omar DIOUF	Omar DIOUF	Vincent BUCHOUX	Thierry DOLIGEZ
Date	April 25, 2016	April 25, 2016	April 25, 2016

Sign

This report contains information of the apparatus covered and is not to be distributed outside the laboratory. The validity of this report is dependent on the scope of the test. The user must ensure that the test conditions are maintained. The user must ensure that the test results are not used for any other purpose. The user must ensure that the test results are not used for any other purpose. The user must ensure that the test results are not used for any other purpose.

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Annex 4: Additional details of the certified platform

Platform model name:	ATPL250A	
Platform hardware version:	Motherboard: ATPL250AMB v1, Daughterboard: ATPLCOUP002 v2	
Platform firmware version:	G3_v1.2.2	
Exact part number of all the chips running G3-PLC stack in the certified platform:	Chip #1: ATPL250A-AKU-Y	Chip #2: ATSAM4C16C-AU
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY	MAC, ADP
Hardware version of this chip:	Revision A	Revision A
Software version running on this chip:	(blanc)	G3_v1.2.2
Operating frequency:	72 MHz	120 MHz

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