



# C E R T I F I C A T E

**Certificate registration number:** G3.1712.179.1.A3

**Certificate holder:** Atmel Spain S.L.U.

**Platform designation:** ATPL250A,  
Hardware version Mother Board: ATPL250AMB v1, Daughter Board: ATPLCOUP007 v2,  
Firmware version G3\_v1.3.0

**Certification date:** December 14<sup>th</sup>, 2017

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile CENELEC A and the device was configured as a PAN-Device.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in November – December 2017. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance and interoperability testing according to the test specification referenced by the test report	Laboratoire des Applications Numériques (LAN)	LAN17AF062

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. This certificate is valid from December 14<sup>th</sup>, 2017.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, December 14<sup>th</sup>, 2017

For the G3-PLC Alliance:

  
**Bernard Lassus**  
Chairman



**Madeleine Francillard**  
Chair Certification Program





## Annex 2: Protocol Implementation Conformance Statement (PICS)

### Feature implementation statement

Name	Value	Description
BAND_PLAN	CENELEC A	Indicate the band-plan supported by the device.
FEATURE_PAN_COORDINATOR	FALSE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULATION	TRUE	Indicate if coherent modulation is supported.
FEATURE_EAP_SERVER	FALSE	Indicate if an EAP-PASK server is implemented by the DUT. Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	True / False
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTENCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.

*H. L.*





## Annex 4: Additional details of the certified platform

Platform model name:	ATPL250A	
Platform hardware version:	Mother Board: ATPL250AMB v1 Daughter Board: ATPLCOUP007 v2	
Platform firmware version:	G3_v1.3.0	
Exact part number of all the chips running G3-PLC stack in the certified platform:	Chip #1: ATPL250A-AKU-Y	Chip #2: ATSAM4C16C-AU
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY	MAC, ADP
Hardware version of this chip:	A	B
Software version running on this chip:	(blanc)	G3_v1.3.0
internal CPU frequency:	72 MHz	120 MHz

*Handwritten initials/signature*