



C E R T I F I C A T E

Certificate registration number: G3.1802.188.1.A3

Certificate holder: Eastsoft Communication Technology Co., Ltd.

Platform designation: ESPLC-CEN,
Hardware version ESPLS1653-G3(V1.1), Firmware version ESG3-CENCV0001

Certification date: February 7th, 2018

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017).

The certificate applies to certification profile Cenelec A and the device was configured as a PAN-Coordinator.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by TÜVRheinland in Yokohama, Japan in January 2018. The results and remarks can be found in the complete test report.

Table with 3 columns: Applied tests, Performed by, Document evidence. Row 1: Conformance and interoperability testing according to the test specification referenced by the test report, TÜVRheinland, 50117304 002

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. This certificate is valid from February 7th, 2018.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, February 7th, 2018

For the G3-PLC Alliance:

Signature of Bernard Lassus
Bernard Lassus
Chairman

Signature of Madeleine Francillard

Madeleine Francillard
Chair Certification Program





Annex 2: Protocol Implementation Conformance Statement (PICS)

Feature implementation statement

Name	Value	Description
BAND_PLAN	CENELEC A	Indicate the band-plan supported by the device.
FEATURE_PAN_COORDINATOR	TRUE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULATION	FALSE	Indicate if coherent modulation is supported.
FEATURE_EAP_SERVER	TRUE	Indicate if an EAP-PASK server is implemented by the DUT. Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	True / False
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTENCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.

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Annex 3: Copy of test report cover sheet

Produkte
Products



Prüfbericht-Nr.: <i>Test Report No.:</i>	50117304 002	Auftrags-Nr.: <i>Order No.:</i>	214094327	Seite 1 von of 21 Page 1 von of 21	
Kunden Referenz-Nr.: <i>Client Reference No.:</i>	N/A	Auftragsdatum: <i>Order date:</i>	2017-12-20		
Auftraggeber: <i>Client:</i>	Eastsoft Communication Technology Co., Ltd. 16A Shangqing Road, Qingdao 266023, China				
Gegenstand der Prüfung: <i>Test item:</i>	G3-PLC CENELEC-A Platform (PAN-Coordinator)				
Bezeichnung / Typ-Nr.: <i>Identification / Type No.:</i>	ESPLC-CEN				
Auftrags-Inhalt: <i>Order content:</i>	G3-PLC Certification Test				
Prüfgrundlage: <i>Test specification:</i>	G3-PLC Conformance L1-L2 Tests Suite Specification v0.25 G3-PLC 1-to-1 Interoperability Tests Suite Specification v0.10 G3-PLC Certification Test Procedures v1.11				
Wareneingangsdatum: <i>Date of receipt:</i>	2018-01-15				
Prüfmuster-Nr.: <i>Test sample No.:</i>	A000686225				
Prüfzeitraum: <i>Testing period:</i>	2018-01-18				
Ort der Prüfung: <i>Place of testing:</i>	TÜV Rheinland Japan Ltd. Global Technology Assessment Center 4-25-2 Kita-Yamata, Tsuzuki-ku Yokohama 224-0021, Japan				
Prüflaboratorium: <i>Testing laboratory:</i>	TÜV Rheinland Japan Ltd.				
Prüfergebnis: <i>Test results:</i>	The test item passed the test specification(s).				
Geprüft von: <i>Tested by:</i>	2018-02-02, Tam Tran Thanh 	Kontrolliert von: <i>Reviewed by:</i>	2018-02-05, Shuji Saito 		
Datum: <i>Date</i>	Name / Stellung: <i>Name / Position</i>	Unterschrift: <i>Signature</i>	Datum: <i>Date</i>	Name / Stellung: <i>Name / Position</i>	Unterschrift: <i>Signature</i>
Sonstiges / Other Aspects:					
This test report is correction report for report No. 50117304 001. "Identification" on this page was changed to "ESPLC-CEN", "IUT Name" and "Hardware" under "1.2 General IUT Information" on page 4 were changed to "ESPLC-CEN" and "ESPLS1653-G3(V1.1)" respectively as the client requested.					
Zustand des Prüfgegenstandes bei Anlieferung: <i>Condition of the test item at delivery:</i>			Prüfmuster vollständig und unbeschädigt <i>Test item complete and undamaged</i>		
*Legende: 1 = sehr gut 2 = gut 3 = befriedigend 4 = ausreichend 5 = mangelhaft P(ass) = entspricht o.g. Prüfgrundlage(n) F(ail) = entspricht nicht o.g. Prüfgrundlage(n) N/A = nicht anwendbar N/T = nicht getestet Legend: 1 = very good 2 = good 3 = satisfactory 4 = sufficient 5 = poor P(ass) = passed a.m. test specification(s) F(ail) = failed a.m. test specification(s) N/A = not applicable N/T = not tested					
Dieser Prüfbericht bezieht sich nur auf das o.g. Prüfmuster und darf ohne Genehmigung der Prüfstelle nicht auszugsweise vervielfältigt werden. Dieser Bericht berechtigt nicht zur Verwendung eines Prüfzeichens. <i>This test report only relates to the a. m. test sample. Without permission of the test center this test report is not permitted to be duplicated in extracts. This test report does not entitle to carry any test mark</i>					



Annex 4: Additional details of the certified platform

Platform model name:	ESPLC-CEN
Platform hardware version:	ESPLS1653-G3(V1.1)
Platform firmware version:	ESG3-CENV0001
Exact part number of all the chips running G3-PLC stack in the certified platform:	SSC1653
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY, MAC & 6LOWPAN
Hardware version of this chip:	Version C
Software version running on this chip:	ESG3-CENCV0001
Internal CPU frequency:	24 MHz

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