

#### CERTIFICATE

Certificate registration number: G3.1804.206.1.A3

Certificate holder: Renesas Electronics Corporation

Platform designation: Y-G-OFDM-PLC-X2,

Hardware version UPD809508K8 (Cool Phoenix 2) / RX631, Firmware version CENELEC-A ES5.2

Certification date: April 30<sup>th</sup>, 2018

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile CENELEC A and the device was configured as a PAN-Device.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in April 2018. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance and interoperability testing according to the test specification referenced by the test report	Laboratoire des Applications Numériques (LAN)	LAN18AF032

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. This certificate is valid from April 30<sup>th</sup>, 2018.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, April 30th, 2018

For the G3-PLC Alliance:

Bernard Lassus Chairman

Madeleine Francillard
Chair Certification Program



Authenticity of this certificate can be verified at http://www.g3-plc.com/certified-products-platforms/

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## **Annex 2: Protocol Implementation Conformance Statement (PICS)**

### Feature implementation statement

Name	Value	Description
BAND_PLAN	CENELEC A	Indicate the band-plan supported by the device.
FEATURE_PAN_COORDINATOR	FALSE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULAT ION	TRUE	Indicate if coherent modulation is supported.
FEATURE FAR CERVER	FALSE	Indicate if an EAP-PASK server is implemented by the DUT.
FEATURE_EAP_SERVER		Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	True / False
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTE NCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.

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## Annex 3: Copy of test report cover sheet



**G3-PLC Certification Test Report** 

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LAN18AF032

April 27, 2018

### **G3-PLC Platform Certification Test Report**

Vendor Name

Renesas Electronics Corporation

Model Name

Y-G-OFDM-PLC-X2

0200021

Serial N° HW version

UPD809508K8(Cool Phoenix 2) / RX631

FW version

CENELEC-A ES5.2

Test Report # Date

TR LAN18AF032 Ed.01

April 27, 2018

CONF Tests Specification CONF Tests Suite **IOT Tests Specification IOT Tests Suite** 

version 0.25. 13/09/2017 10/2017 version 2.6. version 0.10. 08/09/2017 version 2.3. 09/2017

Test Tool version 1.8 version 1.10 Tester Modem

Certification Test Procedures version 1.12. 08/04/2018

Certification Profile

A (CENELEC A) **PAN Device** 





Initiation	Date	Description of modification	Ed.
Omar DIOUF	April 25, 2018	Creation	00
Omar DIOUF	April 27, 2018	Hardware version updated	01
	Realised by	Checked by	Approved by
Name	Omar DIOUF	Vincent BUCHOUX	Thierry DOLIGEZ
Date	April 27, 2018	April 27, 2018	April 27, 2018
Sign	8W/	V.RAX	







# Annex 4: Additional details of the certified platform

Platform model name:	Y-G-OFDM-PLC-X2		
Platform hardware version:	UPD809508K8 (Cool Phoenix 2) / RX631		
Platform firmware version:	CENELEC-A ES5.2		
Exact part number of all the chips running G3-PLC stack in the certified platform:	Chip #1: UPD809508K8 (Cool Phoenix 2)	Chip #2: RX631	
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY, MAC	6LowPAN	
Hardware version of this chip:	UPD809508K8 -711 BAA-A	R5F5631EDDFP	
Software version running on this chip:	v0445	ES5.2	
Internal CPU frequency:	16MHz	96 MHz	



