

CERTIFICATE

Certificate registration number: G3.1806.215.1.C3

Certificate holder: STMicroelectronics

Platform designation: ST8500,

Hardware version EVALMODSTST8500-1, Firmware version 4.3.4 - v1251F5F6 - r0.13

Certification date: June 20th, 2018

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile FCC Multipurpose Worldwide and the device was configured as a PAN-Device.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in June 2018. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance and interoperability testing according to the test specification referenced by the test report	Laboratoire des Applications Numériques (LAN)	LAN18AF052

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. This certificate is valid from June 20th, 2018.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, June 20th, 2018

For the G3-PLC Alliance:

Bernard Lassus Chairman

Madeleine Francillard Chair Certification Program

G3-PLCAlliance

Authenticity of this certificate can be verified at http://www.g3-plc.com/certified-products-platforms/

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Annex 2: Protocol Implementation Conformance Statement (PICS)

Feature implementation statement

Name	Value	Description	
BAND_PLAN	FCC	Indicate the band-plan supported by the device.	
FEATURE_PAN_COORDINATOR	FALSE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).	
FEATURE_COHERENT_MODULAT ION	TRUE	Indicate if coherent modulation is supported.	
FEATURE_EAP_SERVER	FALSE	Indicate if an EAP-PASK server is implemented by the DUT.	
		Apply only if FEATURE_PAN_COORDINATOR = true.	
FEATURE_D8PSK_MODULATION	TRUE	True / False	
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.	
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.	
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.	
FEATURE_PREAMBLE_COEXISTE NCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.	

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Annex 3: Copy of test report cover sheet



G3-PLC Certification Test Report

STMICROELECTRONICS

ST8500 HW:EVALMODSTST8500-1 FW: 4.3.4 - v1251F5F6 - r0.13

LAN18AF052

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G3-PLC Platform Certification Test Report

Vendor Name

STMICROELECTRONICS

Model Name

ST8500

Serial N°

E204460

HW version

EVALMODSTST8500-1

FW version

4.3.4 - v1251F5F6 - r0.13

Test Report #

TR LAN18AF052 Ed.00

Date

June 18, 2018

CONF Tests Specification CONF Tests Suite

IOT Tests Specification

IOT Tests Suite

version 0.25. 13/09/2017 version 2.6. 10/2017 version 0.10, 08/09/2017 09/2017

version 2.3.

Test Tool Tester Modem version 1.8 version 1.10

Certification Test Procedures

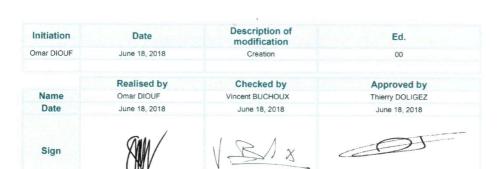
version 1.12. 08/04/2018

Certification Profile

C (FCC)

Overall Verdict

PAN Device PASS





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Annex 4: Additional details of the certified platform

Platform model name:	ST8500	
Platform hardware version:	EVALMODSTST8500-1	
Platform firmware version:	4.3.4 - v1251F5F6 - r0.13	
Exact part number of all the chips running G3-PLC stack in the certified platform:	ST8500	
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY, MAC and 6LowPAN	
Hardware version of this chip:	revBB	
Software version running on this chip:	4.3.4 - v1251F5F6 - r0.13	
internal CPU frequency:	100 MHz for Cortex core and 266 MHz for RTE	

