



# C E R T I F I C A T E

**Certificate registration number:** G3.1806.217.1.A3

**Certificate holder:** Renesas Electronics Corporation

**Platform designation:** REL-G3PLC-CPX3,  
Hardware version R9A06G037GNP, Firmware version 3.03.00

**Certification date:** June 21<sup>st</sup>, 2018

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile CENELEC A and the device was configured as a PAN-Coordinator.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by TÜVRheinland in Yokohama, Japan in June 2018. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance and interoperability testing according to the test specification referenced by the test report	TÜVRheinland	Test report #50146656 001

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. This certificate is valid from June 21<sup>st</sup>, 2018.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, June 21<sup>st</sup>, 2018

For the G3-PLC Alliance:

  
**Bernard Lassus**  
Chairman

  
**Madeleine Francillard**  
Chair Certification Program





# Annex 2: Protocol Implementation Conformance Statement (PICS)

## Feature implementation statement


Name	Value	Description
BAND_PLAN	CENELEC A	Indicate the band-plan supported by the device.
FEATURE_PAN_COORDINATOR	TRUE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULATION	TRUE	Indicate if coherent modulation is supported.
FEATURE_EAP_SERVER	TRUE	Indicate if an EAP-PASK server is implemented by the DUT. Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	True / False
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTENCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.

Handwritten initials/signature

## Annex 3: Copy of test report cover sheet

Produkte  
Products



<b>Prüfbericht-Nr.:</b> Test Report No.:	<b>50146656 001</b>	<b>Auftrags-Nr.:</b> Order No.:	<b>214099706</b>	Seite 1 von of 22 Page 1 von of 22	
<b>Kunden Referenz-Nr.:</b> Client Reference No.:	<b>ICEM3-AE-18-0013-01</b>	<b>Auftragsdatum:</b> Order date:	<b>2018-05-14</b>		
<b>Auftraggeber:</b> Client:	<b>Renesas Electronics Corporation</b>				
<b>Gegenstand der Prüfung:</b> Test item:	<b>G3-PLC CENELEC-A Platform (PAN Coordinator)</b>				
<b>Bezeichnung / Typ-Nr.:</b> Identification / Type No.:	<b>REL-G3PLC-CPX3</b>				
<b>Auftrags-Inhalt:</b> Order content:	<b>G3-PLC Certification Test</b>				
<b>Prüfgrundlage:</b> Test specification:	<b>G3-PLC Conformance L1-L2 Tests Suite Specification v0.25 G3-PLC 1-to-1 Interoperability Tests Suite Specification v0.10 G3-PLC Certification Test Procedures v1.11</b>				
<b>Wareneingangsdatum:</b> Date of receipt:	<b>2018-06-04</b>				
<b>Prüfmuster-Nr.:</b> Test sample No.:	<b>A000752017</b>				
<b>Prüfzeitraum:</b> Testing period:	<b>2018-06-05 – 2018-06-11</b>				
<b>Ort der Prüfung:</b> Place of testing:	<b>TÜV Rheinland Japan Ltd. Global Technology Assessment Center 4-25-2 Kita-Yamata, Tsuzuki-ku Yokohama 224-0021, Japan</b>				
<b>Prüflaboratorium:</b> Testing laboratory:	<b>TÜV Rheinland Japan Ltd.</b>				
<b>Prüfergebnis:</b> Test results:	<i>The test item passed the test specification(s).</i>				
<b>Geprüft von:</b> Tested by:	<b>2018-06-12, Tam Tran Thanh</b>	<b>Kontrolliert von:</b> Reviewed by:	<b>2018-06-15, Endo Atsuhiko</b>		
<b>Datum:</b> Date	<b>Name / Stellung:</b> Name / Position	<b>Unterschrift:</b> Signature	<b>Datum:</b> Date	<b>Name / Stellung:</b> Name / Position	<b>Unterschrift:</b> Signature
<b>Sonstiges / Other Aspects:</b>					
<b>Zustand des Prüfgegenstandes bei Anlieferung:</b> Condition of the test item at delivery:			<b>Prüfmuster vollständig und unbeschädigt</b> Test item complete and undamaged		
<p>*Legende: 1 = sehr gut    2 = gut    3 = befriedigend    4 = ausreichend    5 = mangelhaft  P(ass) = entspricht o.g. Prüfgrundlage(n) F(ail) = entspricht nicht o.g. Prüfgrundlage(n) N/A = nicht anwendbar N/T = nicht getestet  Legend: 1 = very good    2 = good    3 = satisfactory    4 = sufficient    5 = poor  P(ass) = passed a.m. test specification(s) F(ail) = failed a.m. test specification(s) N/A = not applicable N/T = not tested</p>					
<p><b>Dieser Prüfbericht bezieht sich nur auf das o.g. Prüfmuster und darf ohne Genehmigung der Prüfstelle nicht auszugsweise vervielfältigt werden. Dieser Bericht berechtigt nicht zur Verwendung eines Prüfzeichens.</b>  <i>This test report only relates to the a. m. test sample. Without permission of the test center this test report is not permitted to be duplicated in extracts. This test report does not entitle to carry any test mark</i></p>					

*Handwritten initials*



## Annex 4: Additional details of the certified platform

Platform model name:	REL-G3PLC-CPX3
Platform hardware version:	R9A06G037GNP
Platform firmware version:	3.03.00
Exact part number of all the chips running G3-PLC stack in the certified platform:	R9A06G037GNP#AA0
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY, Lower MAC and 6LowPAN
Hardware version of this chip:	R9A06G037GNP
Software version running on this chip:	v0300
Internal CPU frequency:	92 MHz

*Handwritten initials*