

$C \quad E \quad R \quad T \quad I \quad F \quad I \quad C \quad A \quad T \quad E$

Certificate registration number: G3.1810.248.1.A3

Certificate holder: Semtech Corporation

Platform designation: ENIGMA,

Hardware version EV8000, Firmware version 5.0

Certification date: October 29th, 2018

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile CENELEC A and the device was configured as a PAN-Coordinator.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in July 2018. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance and interoperability testing according to the test specification referenced by the test report	Laboratoire des Applications Numériques (LAN)	LAN18AF025

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. This certificate is valid from October 29th, 2018.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, October 29th, 2018

For the G3-PLC Alliance:

Bernard Lassus Chairman

Madeleine Francillard Chair Certification Program





Annex 2: Protocol Implementation Conformance Statement (PICS)

Feature implementation statement

Name	Value	Description
BAND_PLAN	CENELEC A	Indicate the band-plan supported by the device.
FEATURE_PAN_COORDINATOR	TRUE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULAT ION	TRUE	Indicate if coherent modulation is supported.
FEATURE_EAP_SERVER	TRUE	Indicate if an EAP-PASK server is implemented by the DUT.
		Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	True / False
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTE NCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.





Annex 3: Copy of test report cover sheet



G3-PLC Certification Test Report

Semtech Corp ENIGMA HW EV8600 FW 5 0

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G3-PLC Platform Certification Test Report

Vendor Name

Semtech Corp.

Model Name

ENIGMA 325539

Serial N° HW version FW version

EV8000 5.0

Test Report # Date

IOT Tests Suite

TR_LAN18AF025 Ed.00

October 25, 2018

CONF Tests Specification CONF Tests Suite **IOT Tests Specification**

version 0.25, 13/09/2017 version 2.6. 10/2017 version 0.10, 08/09/2017 version 2.3. 09/2017

Test Tool version 1.8 Tester Modem version 1.10

Certification Test Procedures version 1.12. 08/04/2018

A (CENELEC A) Certification Profile Role **PAN Coordinator**

PASS Overall Verdict



Initiation	Date	Description of modification	Ed.
Omar DIQUF	October 25, 2018	Creation	00
	Realised by	Checked by	Approved by
Name	Omar DIOUF	Vincent BUCHOUX	Thierry DOLIGEZ
Date	October 25, 2018	October 25, 2018	October 25, 2018
Sign	8W/	VENX	

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Annex 4: Additional details of the certified platform

Platform model name:	ENIGMA
Platform hardware version:	EV8000
Platform firmware version:	5.0
Exact part number of all the chips running G3-PLC stack in the certified platform:	EV8000ILPT
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY, MAC and 6LowPAN
Hardware version of this chip:	EV8000
Software version running on this chip:	5.0
Internal CPU frequency:	240 MHz



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