# G3-PLC CERTIFIED FCC

## C E R T I F I C A T E

**Certificate registration number:** G3.1811.261.1.C3

**Certificate holder:** Semitech Semiconductor Pty. Ltd.

**Platform designation:** SM8410 EVK,
Hardware version SM8410-3AG,
Firmware version img-g3-fcc-asl-evk2_opa564-v0.10.0.7914_7917M-0.bundle

**Certification date:** November 27th, 2018

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile FCC Multipurpose Worldwide and the device was configured as a PAN-Device.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in January 2018. The results and remarks can be found in the complete test report.

| Applied tests | Performed by | Document evidence |
|---|---|---|
| Conformance and interoperability testing according to the test specification referenced by the test report | Laboratoire des Applications Numériques (LAN) | LAN18AF006 |

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. This certificate is valid from November 27th, 2018.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, November 27th, 2018

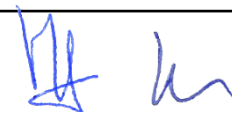For the G3-PLC Alliance:

**Bernard Lassus**
Chairman

**Madeleine Francillard**
Chair Certification Program

## G3-PLC Alliance

# Annex 2: Protocol Implementation Conformance Statement (PICS)

## Feature implementation statement

| Name | Value | Description |
|---|---|---|
| BAND_PLAN | FCC | Indicate the band-plan supported by the device. |
| FEATURE_PAN_COORDINATOR | FALSE | Indicate if the device is a PAN-Coordinator (true) or a normal device (false). |
| FEATURE_COHERENT_MODULATION | TRUE | Indicate if coherent modulation is supported. |
| FEATURE_EAP_SERVER | FALSE | Indicate if an EAP-PASK server is implemented by the DUT. Apply only if FEATURE_PAN_COORDINATOR = true. |
| FEATURE_D8PSK_MODULATION | TRUE | True / False |
| FEATURE_ROUTING | TRUE | Indicate if the routing is implemented by the IUT. |
| FEATURE_SECURITY | F1 | Indicate the security implemented by the device. Possible values are: F1, F2. |
| FEATURE_ACTIVE_SCAN | TRUE | Indicate if the active scan process is done by the IUT after power-up. |
| FEATURE_PREAMBLE_COEXISTENCE_MECHANISM | FALSE | Indicate if the preamble-based coexistence mechanism is used by the IUT. |

# Annex 3:
# Copy of test report cover sheet

## G3-PLC Certification Test Report

| LAN — Your digital lab | | | |
|---|---|---|---|
| SEMITECH | SM8410 EVK HW : SM8410-3AG FW : img-g3-fcc-asl-evk2_opa564-v0.10.0.7914_7917M-0.bundle | | |
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## G3-PLC Alliance
## G3-PLC Platform Certification Test Report

| | |
|---|---|
| Vendor Name | **SEMITECH** |
| Model Name | **SM8410 EVK** |
| Serial N° | **0058CD33** |
| HW version | **SM8410-3AG** |
| FW version | **img-g3-fcc-asl-evk2_opa564-v0.10.0.7914_7917M-0.bundle** |

| | |
|---|---|
| Test Report # | **TR_LAN18AF006 Ed.00** |
| Date | **February 6, 2018** |

| | |
|---|---|
| CONF Tests Specification | **version 0.25. 13/09/2017** |
| CONF Tests Suite | **version 2.6.    10/2017** |
| IOT Tests Specification | **version 0.10. 08/09/2017** |
| IOT Tests Suite | **version 2.3.    09/2017** |

| | |
|---|---|
| Test Tool | **version 1.8** |
| Tester Modem | **version 1.10** |
| Certification Test Procedures | **version 1.11.  11/09/2017** |

| | |
|---|---|
| Certification Profile | **C (FCC)** |
| Role | **PAN Device** |
| Overall Verdict | **PASS** |



| Initiation | Date | Description of modification | Ed. |
|---|---|---|---|
| Omar DIOUF | February 6, 2018 | Creation | 00 |
| | | | |

| | Realised by | Checked by | Approved by |
|---|---|---|---|
| Name | Omar DIOUF | Vincent BUCHOUX | Thierry DOLIGEZ |
| Date | February 6, 2018 | February 6, 2018 | February 6, 2018 |
| Sign | | | |

# Annex 4: Additional details of the certified platform

| | |
|---|---|
| **Platform model name:** | SM8410 EVK |
| **Platform hardware version:** | SM8410-3AG |
| **Platform firmware version:** | img-g3-fcc-asl-evk2_opa564-v0.10.0.7914_7917M-0.bundle |
| **Exact part number of all the chips running G3-PLC stack in the certified platform:** | SM2400 QMR489.1 1601 |
| **What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:** | PHY, MAC and 6LowPAN |
| **Hardware version of this chip:** | QMR489.1 1601 |
| **Software version running on this chip:** | img-g3-fcc-asl-evk2_opa564-v0.10.0.7914_7917M-0.bundle |
| **Internal CPU frequency:** | 90 MHz |