

#### Certificate registration number: G3.1905.290.1.A4

Certificate holder: Hitrend Technology (Shanghai) Co., LTD

**Platform designation:** HT-G3-CENA, Hardware version VER.A.01.00\_0913, Firmware version 0.1.06

#### Certification date: May 17<sup>th</sup>, 2019

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile CENELEC A and the device was configured as a PAN-Device.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by TÜV Rheinland in Yokohama, Japan in April 2019. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance, interoperability and performance testing according to the test specification referenced by the test report	TÜV Rheinland Japan	50227566 002

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. The Protocol Implementation Conformance Statement in the Annex includes the PICS related to performance and is an integral part of this certificate. This certificate is valid from May 17<sup>th</sup>, 2019.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC<sup>™</sup> logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, May 17th, 2019

For the G3-PLC Alliance:

Marc Delandre Chairman



Madeleine Francillard Chair Certification Program



Authenticity of this certificate can be verified at http://www.g3-plc.com/certified-products-platforms/ Pag

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## Annex 1: Protocol Implementation Conformance Statement (PICS)

G3-PLC CENTIFIED

#### Feature implementation statement

Name	Value	Description
BAND_PLAN	CENELEC A	Indicate the band-plan supported by the device.
FEATURE_PAN_COORDINATOR	FALSE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULAT	TRUE	Indicate if coherent modulation is supported.
FEATURE_EAP_SERVER	FALSE	Indicate if an EAP-PASK server is implemented by the DUT.
	FALSE	Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	True / False
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTE NCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.
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## Annex 2: Protocol Implementation Conformance Statement (PICS)

### **PICS related to performance**

The device tested is a G3-PLC CENELEC A platform. Operating voltage applied for certification testing was 220V / 50Hz.

Name	Value	Unit	Descrip	otion		
PICS	relat a thro	ed to re av ugh '	o performano vailable vendor only.	ce		
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# Annex 3: Copy of test report cover sheet

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Produkte Products			E	τυν	Rheinland®	
Prüfbericht-Nr.: Test Report No.:	50227	566 002	Auftrags-Nr.: Order No.:	150107730	Seite 1 von <i>of</i> 41 <i>Page</i> 1 von <i>of</i> 41	
Kunden Referenz-N Client Reference No	N/A		Auftragsda Order date:		02-22	
Auftraggeber: Client:			(Shanghai) Co., Lī ong road 1388, Pu	nghai) Co., LTD road 1388, Pudong New Area, Shanghai, China		
Gegenstand der Pr Test item:	üfung: G3	-PLC CENELEC-A	Platform (PAN De	evice)		
Bezeichnung / Type		-G3-CENA				
Auftrags-Inhalt: Order content:	G3	-PLC Certification	Test			
Prüfgrundlage: Test specification:	G3 G3	PLC 1-to-1 Intero	e L1-L2 Test Suite perability Test Sui e Test Suite Speci Test Procedures	ite Specificat fication v0.27	ion v0.13	
Wareneingangsdat Date of receipt:	:um: 201	9-04-08				
Prüfmuster-Nr.: Test sample No.:	A00	0912526				
Prüfzeitraum: Testing period:	201	9-04-19 – 2019-04	-22			
Ort der Prüfung: Place of testing:	Glol Ass Kita	/ Rheinland Japar bal Technology essment Center 4- -Yamata, Tsuzuki- ohama 224-0021,	25-2 ku			
Prüflaboratorium: Testing laboratory:	ΤÜ\	/ Rheinland Japan	Ltd.	15		
Prüfergebnis: Test results:		test item passed specification(s).	the	Self Long	- and the second se	
Geprüft von Tested by:		Man	Kontrollier Reviewed b		Sturger	
2019-04-25, Tam Tr	an Thanh		2019-05-08,	Atsuhiro Endo	)	
Date N	lame / Stellung lame / Position	Unterschrift Signature	Datum Date	Name / Stel Name / Posi		
Sonstiges / Other A Zustand des Prüfg Condition of the test	egenstandes bei	i Anlieferung:		vollständig un	nd unbeschädigt	
*Legende: 1 = sehr gut P(ass) = entspricht o.g. Legend: 1 = very goo P(ass) = passed a.m. tes Dieser Prüfberich auszugsweise v	2 = gut Prüfgrundlage(n) F(d d 2 = good t specification(s) F( <b>ht bezieht sich nur</b> vervielfältigt werde alates to the a. m. te	3 = satisfactory (ail) = failed a.m. test sp auf das o.g. Prüfme en. Dieser Bericht b est sample. Without p	4 = ausreicher o.g. Prüfgrundlage(n) N 4 = sufficient becification(s) N/A = no uster und darf ohne berechtigt nicht zur N	nd 5: I/A = nicht anwe 5 at applicable A Genehmigung /erwendung ein center this test	= mangelhaft ndbar N/T = nicht geteste = poor VT = not tested <b>der Prüfstelle nicht</b> <b>nes Prüfzeichens</b> . report is not permitted to	

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# Annex 4: Additional details of the certified platform

G3-PLC CENA

Platform model name:	HT-G3-CENA	
Platform hardware version:	VER.A.01.00_0913	
Platform firmware version:	0.1.06	
Exact part number of all the chips running G3-PLC stack in the certified platform:	HT8922	
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY, LowerMAC, 6LoWPAN, ADP	
Hardware version of this chip:	0x035886_8922_01	
Software version running on this chip:	0.1.06	
Internal CPU frequency:	24 MHz	

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