

CERTIFICATE

Certificate registration number: G3.1905.293.1.C4

Certificate holder: Hitrend Technology (Shanghai) Co., LTD

Platform designation: HT-G3-FCC,

Hardware version VER.A.01.00 0913, Firmware version 0.1.00

Certification date: May 17th, 2019

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile FCC Multipurpose Worldwide and the device was configured as a PAN-Coordinator.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by TÜV Rheinland in Yokohama, Japan in April 2019. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance, interoperability and performance testing according to the test specification referenced by the test report	TÜV Rheinland Japan	50227565 001

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. The Protocol Implementation Conformance Statement in the Annex includes the PICS related to performance and is an integral part of this certificate. This certificate is valid from May 17th, 2019.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, May 17th, 2019

For the G3-PLC Alliance:

Maic Delandre Chairman Madeleine Francillard
Chair Certification Program





Annex 1: Protocol Implementation Conformance Statement (PICS)

Feature implementation statement

Name	Value	Description	
BAND_PLAN	FCC	Indicate the band-plan supported by the device.	
FEATURE_PAN_COORDINATOR	TRUE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).	
FEATURE_COHERENT_MODULAT ION	TRUE	Indicate if coherent modulation is supported.	
FEATURE_EAP_SERVER	TRUE	Indicate if an EAP-PASK server is implemented by the DUT.	
		Apply only if FEATURE_PAN_COORDINATOR = true.	
FEATURE_D8PSK_MODULATION	TRUE	True / False	
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.	
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.	
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.	
FEATURE_PREAMBLE_COEXISTE NCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.	
		The man	



Annex 2: Protocol Implementation Conformance Statement (PICS)

PICS related to performance

The device tested is a G3-PLC FCC platform. Operating voltage applied for certification testing was 220V / 50Hz.

My Mass

Certificate registration number: G3.1905.293.1.C4

Page 3 of 5



Annex 3: Copy of test report cover sheet

Producto



Products		_	THE PART OF THE PART OF THE	iciilialia
Prüfbericht-Nr.: Test Report No.:	50227565 001	Auftrags-Nr.: Order No.:	4 = 0 4 0 = = 0 0	Seite 1 von <i>of</i> 41 Page 1 von <i>of</i> 41
Kunden Referenz-Nr.: Client Reference No.:	N/A	N/A Auftragsdatum: 2019-02-22		
Auftraggeber:	Hitrend Technology (Shanghai) Co., LTD			
Client:	Building 16, Zhango	dong road 1388, Po	udong New Area, S	Shanghai, China
Gegenstand der Prüfung: Test item:	G3-PLC FCC Platform (PAN Coordinator)			
Bezeichnung / Typ-Nr.: Identification / Type No.:	HT-G3-FCC			
Auftrags-Inhalt: Order content:	G3-PLC Certificatio	n Test		
Prüfgrundlage: Test specification:	G3-PLC Conforman G3-PLC 1-to-1 Inter G3-PLC Performand G3-PLC Certificatio	operability Test Suce Test Suite Spec	uite Specification vification v	
Wareneingangsdatum: Date of receipt:	2019-04-08			60
Prüfmuster-Nr.: Test sample No.:	A000912526			200
Prüfzeitraum: Testing period:	2019-04-12 – 2019-0	4-15		
Ort der Prüfung: Place of testing:	TÜV Rheinland Japa Global Technology Assessment Center 4 Kita-Yamata, Tsuzuki Yokohama 224-0021	-25-2 i-ku		
Prüflaboratorium: Testing laboratory:	TÜV Rheinland Japar	n Ltd.		
Prüfergebnis: Test results:	The test item passed test specification(s).	the		Mary Control
Geprüft von Tested by:	Man	Kontrollie Reviewed		Study
2019-04-25, Tam Tran Tha	nh	2019-05-09	, Atsuhiro Endo	
	Stellung Unterschrift	Datum	Name / Stellung	Unterschrift

Zustand des Prüfgegenstandes bei Anlieferung:

Prüfmuster vollständig und unbeschädigt Test item complete and undamaged

Condition of the test item at delivery:

Test item complete and undamaged

*Legende: 1 = sehr gut 2 = gut 3 = befriedigend 4 = ausreichend 5 = mangelhaft

P(ass) = entspricht o.g. Prüfgrundlage(n) F(ail) = entspricht nicht o.g. Prüfgrundlage(n) N/A = nicht anwendbar N/T = nicht getestet

Legend: 1 = very good 2 = good 3 = satisfactory 4 = sufficient 5 = poor

P(ass) = passed a.m. test specification(s) F(ail) = failed a.m. test specification(s) N/A = not applicable N/T = not tested

Dieser Prüfbericht bezieht sich nur auf das o.g. Prüfmuster und darf ohne Genehmigung der Prüfstelle nicht auszugsweise vervielfältigt werden. Dieser Bericht berechtigt nicht zur Verwendung eines Prüfzeichens.

This test report only relates to the a. m. test sample. Without permission of the test center this test report is not permitted to

be duplicated in extracts. This test report does not entitle to carry any test mark

Certificate registration number: G3.1905.293.1.C4

Page 4 of 5



Annex 4: Additional details of the certified platform

Platform model name:	HT-G3-FCC	
Platform hardware version:	VER.A.01.00_0913	
Platform firmware version:	0.1.00	
Exact part number of all the chips running G3-PLC stack in the certified platform:	HT8922	
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY, LowerMAC, 6LoWPAN, ADP	
Hardware version of this chip:	0x035886_8922_01	
Software version running on this chip:	0.1.00	
Internal CPU frequency:	24 MHz	

1

Mg