

Certificate registration number: G3.1911.349.1.C4

Certificate holder: Qingdao Eastsoft Communication Technology Co., Ltd.

Platform designation: ESPLC-FCC, Hardware version ESDS55-61-HW(v1.1), Firmware version ESG3SC-V000001 (PC tool: 2016-04-01-10:00)

Certification date: November 20th, 2019

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017).

The certificate applies to certification profile FCC Multipurpose Worldwide and the device was configured as a PAN-Coordinator.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by TÜV Rheinland in Yokohama, Japan in October 2019. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance, interoperability and performance testing according to the test specification referenced by the test report	TÜV Rheinland Japan	50303654 001

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. The Protocol Implementation Conformance Statement in the Annex includes the PICS related to performance and is an integral part of this certificate. This certificate is valid from November 20th, 2019.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC[™] logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, November 20th, 2019

For the G3-PLC Alliance:

Marc Delandre Chairman

Madeleine Francillard Chair Certification Program



Authenticity of this certificate can be verified at http://www.g3-plc.com/certified-products-platforms/ Pag

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Annex 1: Protocol Implementation Conformance Statement (PICS)

G3-PLC FCC

Feature implementation statement

Name	Value	Description	
BAND_PLAN	FCC	Indicate the band-plan supported by the device.	
FEATURE_PAN_COORDINATOR	TRUE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).	
FEATURE_COHERENT_MODULAT	FALSE	Indicate if coherent modulation is supported.	
FEATURE_EAP_SERVER	TRUE	Indicate if an EAP-PASK server is implemented by the DUT.	
		Apply only if FEATURE_PAN_COORDINATOR = true.	
FEATURE_D8PSK_MODULATION	TRUE	True / False	
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.	
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.	
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.	
FEATURE_PREAMBLE_COEXISTE NCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.	
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Annex 2: Protocol Implementation Conformance Statement (PICS)

G3-PLC FCC

PICS related to performance

The device tested is a G3-PLC FCC platform. Operating voltage applied for certification testing was 220V / 50Hz.

Name	Value	Unit	Descript	ion		
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Annex 3:

G3-PLC

Produkte

CERTIFIED

FCC

Copy of test report cover sheet

TÜVRheinland® Products Prüfbericht-Nr.: Auftrags-Nr.: Seite 1 von of 40 50303654 001 150116838 Test Report No.: Order No. Page 1 von of 40 Kunden Referenz-Nr.: Auftragsdatum: N/A 2019-10-15 Client Reference No. Order date: Qingdao Eastsoft Communication Technology Co., Ltd. Auftraggeber: Client: 16A Shangqing Road, Qingdao 266023, China Gegenstand der Prüfung: G3-PLC FCC Platform (PAN Coordinator) Test item: Bezeichnung / Typ-Nr.: ESPLC-FCC Identification / Type No .: Auftrags-Inhalt: G3-PLC Certification Test Order content: G3-PLC Conformance L1-L2 Test Suite Specification v0.30 Prüfgrundlage: G3-PLC 1-to-1 Interoperability Test Suite Specification v0.13 G3-PLC Performance Test Suite Specification v0.27 Test specification: G3-PLC Certification Test Procedures v1.14 Wareneingangsdatum: 2019-10-28 Date of receipt. Prüfmuster-Nr.: A001020670 Test sample No.: Prüfzeitraum: 2019-10-29 - 2019-10-30 Testing period: Ort der Prüfung: TÜV Rheinland Japan Ltd. Place of testing: Global Technology Assessment Center 4-25-2 Kita-Yamata, Tsuzuki-ku Yokohama 224-0021, Japan Prüflaboratorium: TÜV Rheinland Japan Ltd. Testing laboratory: Prüfergebnis: The test item passed the Test results. test specification(s) Geprüft von Kontrolliert von Tested by: Reviewed by: tuoting 2019-11-12, Atsuhiro Endo 2019-11-12, Tam Tran Thanh Name / Stellung Unterschrift Datum Name / Stellung Unterschrift Datum Name / Position Signature Date Name / Position Signature Date Sonstiges / Other Aspects: Zustand des Prüfgegenstandes bei Anlieferung: Prüfmuster vollständig und unbeschädigt Condition of the test item at delivery: Test item complete and undamaged 1 = sehr gut 4 = ausreichend *Legende: 2 = gut 3 = befriedigend 5 = mangelhaft P(ass) = entspricht o.g. Prüfgrundlage(n) F(ail) = entspricht nicht o.g. Prüfgrundlage(n) N/A = nicht anwendbar N/T = nicht getestet 3 = satisfactory Legend: 1 = very good 2 = good4 = sufficient 5 = poor P(ass) = passed a.m. test specification(s) F(ail) = failed a.m. test specification(s) N/A = not applicable N/T = not testedDieser Prüfbericht bezieht sich nur auf das o.g. Prüfmuster und darf ohne Genehmigung der Prüfstelle nicht auszugsweise vervielfältigt werden. Dieser Bericht berechtigt nicht zur Verwendung eines Prüfzeichens. This test report only relates to the a.m. test sample. Without permission of the test center this test report is not permitted to be duplicated in extracts. This test report does not entitle to carry any test mark

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Annex 4: Additional details of the certified platform

G3-PLC FCC

Platform model name:	ESPLC-FCC	
Platform hardware version:	ESDS55-61-HW(v1.1)	
Platform firmware version:	ESG3SC-V000001 (PC tool: 2016-04-01-10:00)	
Exact part number of all the chips running G3-PLC stack in the certified platform:	SSC1655NQ	
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PHY, MAC and 6LowPAN	
Hardware version of this chip:	Version A	
Software version running on this chip:	ESG3SC-V000001	
Internal CPU frequency:	20 MHz	

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