

Certificate registration number: G3.2111.471.1.C5

Certificate holder: STMicroelectronics S.r.l

Platform designation: ST8500 + S2-LP,

Hardware version EVALMODSTST8500-1, Firmware version 6.1.20 v15a0c666 r1.9rc1

Certification date: November 5th, 2021

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the reference specification ITU G.9903 (08-2017). The optional features Hybrid PLC&RF and coherent mode of the G3-PLC protocol are also covered by this certification.

The reference version for the optional hybrid feature is the G3-PLC Hybrid PLC&RF companion specification v0.11 (31-07-2020). The certificate applies to certification profile FCC Multipurpose Worldwide and the device was configured as a PAN-Coordinator.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in October 2021. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance, interoperability and performance testing according to the test specification referenced by the test report	Laboratoire des Applications Numériques (LAN)	LAN21AF059

The device tested is a G3-PLC Hybrid PLC&RF platform: a solution providing an implementation of the G3-PLC specification. The Protocol Implementation Conformance Statement in the Annex includes the PICS related to performance and is an integral part of this certificate. This certificate is valid from November 5th, 2021.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC[™] logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, November 5th 021

For the G3-PLC Alliance:

Marc Delandre Chairman

Madeleine Francillard Chair Certification Program



Authenticity of this certificate can be verified at https://www.g3-plc.com/g3-plc-certification/ Page 1 of 5

Annex 1: Protocol Implementation Conformance Statement (PICS)

G3-PLC F(C ())

Feature implementation statement

Name	Value	Description	
BAND_PLAN	FCC	Indicate the band-plan supported by the device.	
BAND_PLAN_RF	863-870 MHz, mode #1	Indicate the RF band-plan.	
FEATURE_HYBRID_RF	TRUE	Indicate if Hybrid PLC&RF feature is supported.	
FEATURE_PAN_COORDINATOR	TRUE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).	
FEATURE_COHERENT_MODULAT	TRUE	Indicate if coherent modulation is supported.	
	TRUE	Indicate if an EAP-PASK server is implemented by the DUT.	
FEATURE_EAP_SERVER		Apply only if FEATURE_PAN_COORDINATOR = true.	
FEATURE_D8PSK_MODULATION	TRUE	True / False	
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.	
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.	
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.	
FEATURE_PREAMBLE_COEXISTE NCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.	

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Annex 2: Protocol Implementation Conformance Statement (PICS)

G3-PLC FC (

PICS related to performance

The device tested is a G3-PLC FCC platform. Testing was performed on phase 1. Operating voltage applied for certification testing was 230V/50Hz .

	Name	Value	Unit	Descrip	tion		
	PICS r	elat a thro	ed to re av ugh '	o performano vailable vendor only.	ce		
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Annex 3: Copy of test report cover sheet

G3-PLC F((III)

G3-PLC Certification Test Report

ST8500 + S2-LP HW:EVALMODSTST8500-1 FW: 6.1.20 v15a0c666 r1.9rc1

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G3-PLC Alliance **G3-PLC Hybrid Platform Certification Test Report**

Vendor Name

Model Name Serial N° HW version FW version

STMicroelectronics S.r.I ST8500 + S2-LP CL374-1.3 EVALMODSTST8500-1 6.1.20 v15a0c666 r1.9rc1

Test Report # Date

TR_LAN21AF059 Ed.00 November 5, 2021

CONF G3-PLC Tests Specification CONF G3-PLC Tests Suite **CONF HYBRID Tests Specification** CONF HYBRID Tests Suite IOT G3-PLC Tests Specification IOT G3-PLC Tests Suite IOT HYBRID Tests Specification IOT RF Tests Suite PERF G3-PLC Tests Specification PERF G3-PLC Tests Suite

Test Tool PLC version 2.3 Tester Modem PLC version 2.0 Test Tool PLC+RF version 3.1 Tester Modem PLC+RF version 7 Certification Test Procedures version 2.0

Certification Profile Role **Overall Verdict**

HYBRID : FCC - RF **PAN Coordinator** PASS

version 0.32. 15/03/2021 version 2.11.p2. 04/2021 version 0.7. 15/03/2021 version 1.2.p5. 07/2021 version 0.13. 28/12/2018 version 2.6.p2. 03/2021 version 0.4. 15/03/2021 version 1.2.p1. 03/2021 version 0.27. 05/03/2019 version 2.11.p2. 04/2021

12/04/2021



24 The current report and the test results produced in this current are given for information only and must not be relied on by any third person for any reason. This report contains an assessment of the apparatus carried out on samples submitted to the laboratory. The results in this report relate only to the items tested and were obtained in the period between the initial receipt of samples and the issue of the report. It should be noted that technical hardware or software modifications on the apparatus may impact the results reported in this document.

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Annex 4: Additional details of the certified platform

G3-PLC F(C III)

Platform model name:	ST8500 + S2-LP		
Platform hardware version:	EVALMODSTST8500-1		
Platform firmware version:	6.1.20 v15a0c666 r1.9rc1		
Exact part number of all the chips running G3-PLC stack in the certified platform:	Chip #1: ST8500	Chip #2: S2-LP	
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PLC PHY, PLC MAC, RF MAC, 6LowPAN	RF PHY	
Hardware version of this chip:	revBB	2.1	
Software version running on this chip:	6.1.20 v15a0c666 r1.9rc1	n/a	
Internal CPU frequency:	100 MHz for Cortex and 400 MHz for RTE	n/a	
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