



C E R T I F I C A T E

Certificate registration number: G3.2205.513.1.A6

Certificate holder: Renesas Electronics Corporation

Platform designation: REL-G3PLC-CPX3+TRG,
Hardware version R9A06G037GNP+RAA604S002GNP,
Firmware version 3.05.04

Certification date: May 9th, 2022

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the G3-PLC Alliance reference specification 06/2021. The optional features Hybrid PLC&RF and coherent mode of the G3-PLC protocol are also covered by this certification.

The certificate applies to certification profile CENELEC A and the device was configured as a PAN-Coordinator.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by TÜV Rheinland in Yokohama, Japan in April 2022. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance, interoperability and performance testing according to the test specification referenced by the test report	TÜV Rheinland Japan	JP22SIA2 002

The device tested is a G3-PLC Hybrid PLC&RF platform: a solution providing an implementation of the G3-PLC specification. The Protocol Implementation Conformance Statement in the Annex includes the PICS related to performance and is an integral part of this certificate. This certificate is valid from May 9th, 2022.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, May 9th 2022

For the G3-PLC Alliance:

Marc Delandre
Chairman

Madeleine Francillard
Chair Certification Program



Annex 1: Protocol Implementation Conformance Statement (PICS)

Feature implementation statement

Name	Value	Description
BAND_PLAN	CEN A	Indicate the band-plan supported by the device.
BAND_PLAN_RF	863-870_SingleCarrier_Mode#1 863-870_SingleCarrier_Mode#2 865-868_SingleCarrier_Mode#1 865-868_SingleCarrier_Mode#2 870-876_SingleCarrier_Mode#1 870-876_SingleCarrier_Mode#2	Indicate the RF band plan(s) supported by the device
FEATURE_HYBRID_RF	TRUE	Indicate if Hybrid PLC&RF feature is supported.
FEATURE_PAN_COORDINATOR	TRUE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULATION	TRUE	Indicate if coherent modulation is supported.
FEATURE_EAP_SERVER	TRUE	Indicate if an EAP-PASK server is implemented by the DUT. Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	Indicate if D8PSK modulation is supported
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTENCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.









Annex 2: Protocol Implementation Conformance Statement (PICS)

PICS related to performance

The device tested is a G3-PLC CENELEC A platform. Testing was performed on phase 1. Operating voltage applied for certification testing was 230V/50Hz .

Name	Value	Unit	Description
<p>PICS related to performance are available through vendor only.</p>			

Annex 3: Copy of test report cover sheet

Prüfbericht - Produkte <i>Test Report - Products</i>			
Prüfbericht-Nr.: <i>Test report no.:</i>	JP22SIA2 002	Auftrags-Nr.: <i>Order no.:</i>	150258954 10 Seite 1 von 54 Page 1 of 54
Kunden-Referenz-Nr.: <i>Client reference no.:</i>	IACS-AF-22-0004	Auftragsdatum: <i>Order date:</i>	2022-03-25
Auftraggeber: <i>Client:</i>	Renesas Electronics Corporation 5-20-1 Josuihon-cho, 1878-588 Kodaira-shi, Tokyo, Japan		
Prüfgegenstand: <i>Test item:</i>	G3-PLC Platform CENELEC A PAN Coordinator		
Bezeichnung / Typ-Nr.: <i>Identification / Type no.:</i>	REL-G3PLC-CPX3+TRG		
Auftrags-Inhalt: <i>Order content:</i>	G3PLC		
Prüfgrundlage: <i>Test specification:</i>	G3-PLC Alliance - Conformance Tests Suite Specification - v0.37 G3-PLC Alliance - 1to1-PHY-Interoperability-Tests-Specification-v0.14 G3-PLC Alliance - Performance Test Suite Specification - v0.27 G3-PLC Certification Test Procedures version 6.02 G3-PLC Alliance - Hybrid PLC&RF - Conformance Tests Suite Specification - v0.10 G3-PLC Alliance - 1to1-PHY-RF-Interoperability-Tests-Specification-v0.7		
Wareneingangsdatum: <i>Date of sample receipt:</i>	2022-03-31		
Prüfmuster-Nr.: <i>Test sample no.:</i>	A003236611		
Prüfzeitraum: <i>Testing period:</i>	2022-04-18 – 2022-04-28		
Ort der Prüfung: <i>Place of testing:</i>	4-25-2 Kita-Yamata, Tsuzuki-ku Yokohama 224-0021, Japan		
Prüflaboratorium: <i>Testing laboratory:</i>	TÜV Rheinland Japan Ltd.		
Prüfergebnis*: <i>Test result*:</i>	Pass		
geprüft von: <i>tested by:</i>	 Martin Zietz	genehmigt von: <i>authorized by:</i>	 Shuji Saito
Datum: <i>Date:</i>	2022-05-06	Ausstellungsdatum: <i>Issue date:</i>	2022-05-09
Stellung / Position:	Tester	Stellung / Position:	Reviewer
Sonstiges / Other:	Corrigendum of test report JP22SIA2 001: The model name, serial number and firmware version number of the IOT device in section 2.2.3 was corrected.		
Zustand des Prüfgegenstandes bei Anlieferung: <i>Condition of the test item at delivery:</i>	Prüfmuster vollständig und unbeschädigt <i>Test item complete and undamaged</i>		
* Legende: P(ass) = entspricht o.g. Prüfgrundlage(n) F(ail) = entspricht nicht o.g. Prüfgrundlage(n) N/A = nicht anwendbar N/T = nicht getestet * Legend: P(ass) = passed a.m. test specification(s) F(ail) = failed a.m. test specification(s) N/A = not applicable N/T = not tested			
Dieser Prüfbericht bezieht sich nur auf das o.g. Prüfmuster und darf ohne Genehmigung der Prüfstelle nicht auszugsweise vervielfältigt werden. Dieser Bericht berechtigt nicht zur Verwendung eines Prüfzeichens. <i>This test report only relates to the a. m. test sample. Without permission of the test center this test report is not permitted to be duplicated in extracts. This test report does not entitle to carry any test mark.</i>			
vos TÜV Rheinland Japan Ltd., Global Technology Assessment Center 4-25-2 Kita-Yamata, Tsuzuki-ku Yokohama 224-0021, Japan Mail: g3plc@tuv.com · Web: www.tuv.com/			



Annex 4: Additional details of the certified platform

Platform model name:	REL-G3PLC-CPX3+TRG	
Platform hardware version:	R9A06G037GNP+RAA604S002GNP	
Platform firmware version:	3.05.04	
Exact part number of all the chips running G3-PLC stack in the certified platform:	Chip #1: R9A06G037GNP#AA0	Chip #2: RAA604S002GNP
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PLC PHY, PLC MAC, RF MAC, 6LowPAN	RF PHY
Hardware version of this chip:	R9A06G037GNP	RAA604S002GNP
Software version running on this chip:	v5.04	n/a
Internal CPU frequency:	138 MHz	n/a

