



C E R T I F I C A T E

Certificate registration number: G3.2205.515.1.C6

Certificate holder: STMicroelectronics S.r.l

Platform designation: ST8500 + S2-LP
Hardware version EVALMODSTST8500-1
Firmware version 6.2.5 v164053f3 r1.13rc0

Certification date: May 12th, 2022

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the G3-PLC Alliance reference specification 06/2021. The optional features Hybrid PLC&RF and coherent mode of the G3-PLC protocol are also covered by this certification.

The certificate applies to certification profile FCC Multipurpose Worldwide and the device was configured as a PAN-Device.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in April 2022. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance, interoperability and performance testing according to the test specification referenced by the test report	Laboratoire des Applications Numériques (LAN)	LAN22AF023

The device tested is a G3-PLC Hybrid PLC&RF platform: a solution providing an implementation of the G3-PLC specification. The Protocol Implementation Conformance Statement in the Annex includes the PICS related to performance and is an integral part of this certificate. This certificate is valid from May 12th, 2022.

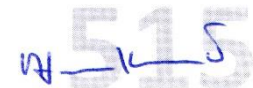
The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, May 12th 2022

For the G3-PLC Alliance:


Marc Delandre
Chairman


515

Madeleine Francillard
Chair Certification Program



Annex 1: Protocol Implementation Conformance Statement (PICS)

Feature implementation statement

Name	Value	Description
BAND_PLAN	FCC	Indicate the band-plan supported by the device.
BAND_PLAN_RF	863-870_SingleCarrier_Mode#1	Indicate the RF band plan(s) supported by the device
FEATURE_HYBRID_RF	TRUE	Indicate if Hybrid PLC&RF feature is supported.
FEATURE_PAN_COORDINATOR	FALSE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULATION	TRUE	Indicate if coherent modulation is supported.
FEATURE_EAP_SERVER	FALSE	Indicate if an EAP-PASK server is implemented by the DUT. Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	Indicate if D8PSK modulation is supported
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTENCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.





Annex 2: Protocol Implementation Conformance Statement (PICS)

PICS related to performance

The device tested is a G3-PLC FCC platform. Testing was performed on phase 1. Operating voltage applied for certification testing was 230V/50Hz .

Name	Value	Unit	Description
<p>PICS related to performance are available through vendor only.</p>			

Annex 3: Copy of test report cover sheet



G3-PLC Certification Test Report

STMicroelectronics S.r.l	ST8500 + S2-LP HW:EVALMODSTST8500-1 FW: 6.2.5 v164053f3 r1.13rc0
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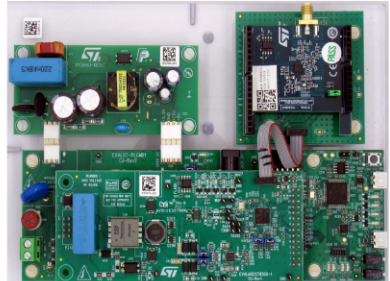
G3-PLC Alliance

G3-PLC Hybrid Platform Certification Test Report

Vendor Name **STMicroelectronics S.r.l**
 Model Name **ST8500 + S2-LP**
 Serial N° **CL561-1.3**
 HW version **EVALMODSTST8500-1**
 FW version **6.2.5 v164053f3 r1.13rc0**

Test Report # **TR_LAN22AF023 Ed.00**
 Date **May 10, 2022**




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|---------------------------------|----------------------|-------------------|
| CONF G3-PLC Tests Specification | version 0.37. | 18/01/2022 |
| CONF G3-PLC Tests Suite | version 2.14. | 02/2022 |
| CONF HYBRID Tests Specification | version 0.10. | 12/12/2021 |
| CONF HYBRID Tests Suite | version 1.3. | 12/2021 |
| IOT G3-PLC Tests Specification | version 0.14. | 11/11/2020 |
| IOT G3-PLC Tests Suite | version 2.7. | 11/2021 |
| IOT HYBRID Tests Specification | version 0.7. | 14/09/2021 |
| IOT RF Tests Suite | version 1.3. | 03/2022 |
| PERF G3-PLC Tests Specification | version 0.27. | 05/03/2019 |
| PERF G3-PLC Tests Suite | version 2.14. | 02/2022 |



Test Tool PLC+RF **version 3.2**
 Tester Modem PLC **version 2.0.1**
 Tester Modem RF **version 11**
 Certification Test Procedures **version 6.03** **04/03/2022**

Certification Profile **HYBRID : FCC - RF**
 Role **PAN Device**
 Overall Verdict **PASS**

Initiation	Date	Description of modification	Ed.
Omar DIOUF	May 10, 2022	Creation	00

	Realised by	Checked by	Approved by
Name	Omar DIOUF	Vincent BUCHOUX	Thierry DOLIGEZ
Date	May 10, 2022	May 10, 2022	May 10, 2022
Sign			

The current report and the test results produced in this current are given for information only and must not be relied on by any third person for any reason.
 This report contains an assessment of the apparatus carried out on samples submitted to the laboratory. The results in this report relate only to the items tested and were obtained in the period between the initial receipt of samples and the issue of the report. It should be noted that technical hardware or software modifications on the apparatus may impact the results reported in this document.



Annex 4: Additional details of the certified platform

Platform model name:	ST8500 + S2-LP	
Platform hardware version:	EVALMODSTST8500-1	
Platform firmware version:	6.2.5 v164053f3 r1.13rc0	
Exact part number of all the chips running G3-PLC stack in the certified platform:	Chip #1: ST8500	Chip #2: S2-LP
What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:	PLC PHY, PLC MAC, RF MAC, 6LowPAN	RF PHY
Hardware version of this chip:	RevBB	2.1
Software version running on this chip:	6.2.5 v164053f3 r1.13rc0	n/a
Internal CPU frequency:	100 MHz for Cortex and 400 MHz for RTE	n/a

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