

#### CERTIFICATE

Certificate registration number: G3.2208.527.1.A6

Certificate holder: Hi-Trend Technology (Shanghai) Co., Ltd.

Platform designation: HT8922,

Hardware version 0x035886 8922 01, Firmware version V2.2.03

Certification date: August 2<sup>nd</sup> 2022

This certificate indicates the above mentioned platform successfully completed certification testing with regards to the G3-PLC Alliance reference specification 06/2021. The optional features Hybrid PLC&RF and coherent mode of the G3-PLC protocol are also covered by this certification.

The certificate applies to certification profile CENELEC A and the device was configured as a PAN-Coordinator.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by TÜV Rheinland in Yokohama, Japan in July 2022. The results and remarks can be found in the complete test report.

| Applied tests   | Performed by        | Document evidence |
|---|---------------------|-------------------|
| Conformance, interoperability and performance testing according to the test specification referenced by the test report | TÜV Rheinland Japan | JP22UOC3 002      |

The device tested is a G3-PLC Hybrid PLC&RF platform: a solution providing an implementation of the G3-PLC specification. The Protocol Implementation Conformance Statement in the Annex includes the PICS related to performance and is an integral part of this certificate. This certificate is valid from August 2<sup>nd</sup> 2022.

The certificate is only applicable to the platform described above and permits the use of the G3-PLC™ logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, August 2<sup>nd</sup> 2022

For the G3-PLC Alliance:

Marc Delandre Chairman MTIOZ

Madeleine Francillard Chair Certification Program





# **Annex 1: Protocol Implementation Conformance Statement (PICS)**

### **Feature implementation statement**

| Name                                       | Value                        | Description   |  |
|--|------------------------------|---|--|
| BAND_PLAN                                  | CENELEC A                    | Indicate the band-plan supported by the device.                                       |  |
|  | 863-870_SingleCarrier_Mode#1 |   |  |
|  | 863-870_SingleCarrier_Mode#2 |   |  |
| DAND DIANI DE                              | 865-868_SingleCarrier_Mode#1 | Indicate the RF band plan(s)  |  |
| BAND_PLAN_RF                               | 865-868_SingleCarrier_Mode#2 | supported by the device   |  |
|  | 870-876_SingleCarrier_Mode#1 |   |  |
|  | 870-876_SingleCarrier_Mode#2 |   |  |
| FEATURE_HYBRID_RF                          | TRUE                         | Indicate if Hybrid PLC&RF feature is supported.                                       |  |
| FEATURE_PAN_<br>COORDINATOR                | TRUE                         | Indicate if the device is a PAN-<br>Coordinator (true) or a normal<br>device (false). |  |
| FEATURE_COHERENT_<br>MODULATION            | TRUE                         | Indicate if coherent modulation is supported.   |  |
|  |                              | Indicate if an EAP-PASK server is implemented by the DUT.                             |  |
| FEATURE_EAP_SERVER                         | TRUE                         | Apply only if FEATURE_PAN_COORDINATOR = true.   |  |
| FEATURE_D8PSK_<br>MODULATION               | TRUE                         | Indicate if D8PSK modulation is supported   |  |
| FEATURE_ROUTING                            | TRUE                         | Indicate if the routing is implemented by the IUT.                                    |  |
| FEATURE_SECURITY                           | F1                           | Indicate the security implemented by the device. Possible values are: F1, F2.         |  |
| FEATURE_ACTIVE_SCAN                        | TRUE                         | Indicate if the active scan process is done by the IUT after power-up.                |  |
| FEATURE_PREAMBLE_<br>COEXISTENCE_MECHANISM | FALSE                        | Indicate if the preamble-based coexistence mechanism is used by the IUT.              |  |

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### **Annex 2: Protocol Implementation Conformance Statement (PICS)**

### PICS related to performance

The device tested is a G3-PLC Cenelec A platform. Testing was performed on phase 1. Operating voltage applied for certification testing was 230V/50Hz.

| Name     | Value | Unit | Description                               |       |
|----------|-------|------|---|-------|
| DICS     | relat | ed t | o performance<br>vailable<br>vendor only. |       |
| <u> </u> |       |      |   | Mu Ju |

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# Annex 3: Copy of test report cover sheet

Prüfbericht - Produkte Test Report - Products



**TÜV**Rheinland®

Prüfbericht-Nr.: JP22UOC3 002 Auftrags-Nr.: Seite 1 von 50 150256792 10 Test report no.: Order no.: Page 1 of 50 Kunden-Referenz-Nr.: HT8922 Auftragsdatum: 2022-02-09 Client reference no.: Order date: Hi-Trend Technology (Shanghai) Co., Ltd. Auftraggeber: Building No.16, No.1388, Zhangdong Road, Shanghai, 201203, China Prüfgegenstand: G3-PLC Platform Test item: (PAN Coordinator, CENELEC A)

Bezeichnung / Typ-Nr.: HT8922
Identification / Type no.:

Auftrags-Inhalt: G3PLC
Order content:

 Prüfgrundlage:
 G3-PLC Alliance - Conformance Tests Suite Specification - v0.37

 Test specification:
 G3-PLC Alliance - 1to1-PHY-Interoperability-Tests-Specification-v0.14

 G3-PLC Alliance - Performance Test Suite Specification - v0.27

 G3-PLC Certification Test Procedures version 6.02

G3-PLC Alliance - Hybrid PLC&RF - Conformance Tests Suite Specification - v0.10 G3-PLC Alliance - 1to1-PHY-RF-Interoperability-Tests-Specification-v0.7

genehmigt von:

authorized by:

HT-PANC-A

Shuji Saito

Reviewer

Wareneingangsdatum:
Date of sample receipt:

Prüfmuster-Nr.:
Test sample no:

A003291105-001 to 015

 Prüfzeitraum:
 2022-07-13 – 2022-07-19

 Testing period:
 2022-07-19

Ort der Prüfung: 4-25-2 Kita-Yamata, Tsuzuki-ku Yokohama 224-0021, Japan

**Prüflaboratorium:**Testing laboratory:

TÜV Rheinland Japan Ltd.

Prüfergebnis\*: Pass Test result\*:

geprüft von:
tested by:

Datum:

Datum:Ausstellungsdatum:Date:2022-08-01Martin ZietzIssue date:2022-08-01Stellung / Position:TesterStellung / Position:

Sonstiges / Other: Corrigendum to test report JP22UOC3 001: The company name and test item description on this page have been corrected as above.

Zustand des Prüfgegenstandes bei Anlieferung: Prüfmuster vollständig und unbeschädigt

Condition of the test item at delivery:

\* Legende: P(ass) = entspricht o.g. Prüfgrundlage(n)

\* Legend: P(ass) = passed a.m. test specification(s)

Dieser Prüfbericht bezieht sich nur auf das o.g. Prüfmuster und darf ohne Genehmigung der Prüfstelle nicht auszugsweise vervielfältigt werden. Dieser Bericht berechtigt nicht zur Verwendung eines Prüfzeichens. This test report only relates to the a. m. test sample. Without permission of the test center this test report is not permitted to be duplicated in extracts. This test report does not entitle to carry any test mark.

vös TÜV Rheinland Japan Ltd., Global Technology Assessment Center 4-25-2 Kita-Yamata, Tsuzuki-ku Yokohama 224-0021, Japan Mail: g3plc@tuv.com · Web: www.tuv.com/

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# Annex 4: Additional details of the certified platform

| Platform model name:  | HT8922             |                    |  |
|---|--------------------|--------------------|--|
| Platform hardware version:  | 0x035886_8922_01   |                    |  |
| Platform firmware version:  | V2.2.03            |                    |  |
| Exact part number of all the chips running G3-PLC stack in the certified platform:        | Chip #1:<br>HT8922 | Chip #2:<br>Si4463 |  |
| What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack: | PHY, lowerMAC, ADP | PHY                |  |
| Hardware version of this chip:  | А                  | C2                 |  |
| Software version running on this chip:  | V2.2.03            | V1.0               |  |
| Internal CPU frequency:   | 24 MHz             | 30 MHz             |  |

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