



# C E R T I F I C A T E

**Certificate registration number:** G3.2310.585.1.A6

**Certificate holder:** Farlink Technology Limited

**Platform designation:** ENV-EV8010-QFN56-1PHASE-A2  
Hardware version EV8010,  
Firmware version 5046-04

**Certification date:** October 27<sup>th</sup> 2023

This certificate indicates the above-mentioned platform successfully completed certification testing with regards to the G3-Alliance reference specification 06/2021. The optional feature coherent mode of the G3-PLC protocol is also covered by this certification.

The certificate applies to certification profile CENELEC A and the device was configured as a PAN-Device.

Test cases have been performed as described in the test report referred to below. This certificate is granted on account of tests conducted by Laboratoire des Applications Numériques (LAN) in Tauxigny, France in October 2023. The results and remarks can be found in the complete test report.

Applied tests	Performed by	Document evidence
Conformance, interoperability and performance testing according to the test specification referenced by the test report	Laboratoire des Applications Numériques (LAN)	LAN23AF030

The device tested is a G3-PLC platform: a solution providing an implementation of the G3-PLC specification. The Protocol Implementation Conformance Statement in the Annex includes the PICS related to performance and is an integral part of this certificate. This certificate is valid from October 27<sup>th</sup> 2023.

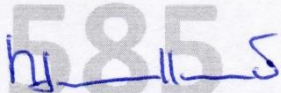
The certificate is only applicable to the platform described above and permits the use of the G3-PLC logo as laid down in the G3-PLC logo license agreement.

This certificate does not imply assessment of the production. This certificate shall not be defined, or used as a guarantee covering quality of a product which includes G3-PLC. The liability of the Alliance and the test laboratory or any of her representatives is excluded for any damages or losses of the certified company.

Paris, October 27<sup>th</sup> 2023

For the G3-Alliance:

  
**Marc Delandre**  
Chairman

  
585

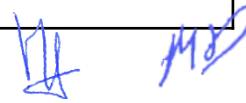
**Madeleine Francillard**  
Chair Certification Program

# G3-Alliance

# Annex 1: Protocol Implementation Conformance Statement (PICS)

## Feature implementation statement

Name	Value	Description
BAND_PLAN	CENELEC A	Indicate the band-plan supported by the device.
BAND_PLAN_RF	n/a	Indicate the RF band plan(s) supported by the device
FEATURE_HYBRID_RF	FALSE	Indicate if Hybrid PLC&RF feature is supported.
FEATURE_PAN_COORDINATOR	FALSE	Indicate if the device is a PAN-Coordinator (true) or a normal device (false).
FEATURE_COHERENT_MODULATION	TRUE	Indicate if coherent modulation is supported.
FEATURE_EAP_SERVER	FALSE	Indicate if an EAP-PASK server is implemented by the DUT. Apply only if FEATURE_PAN_COORDINATOR = true.
FEATURE_D8PSK_MODULATION	TRUE	Indicate if D8PSK modulation is supported
FEATURE_ROUTING	TRUE	Indicate if the routing is implemented by the IUT.
FEATURE_SECURITY	F1	Indicate the security implemented by the device. Possible values are: F1, F2.
FEATURE_ACTIVE_SCAN	TRUE	Indicate if the active scan process is done by the IUT after power-up.
FEATURE_PREAMBLE_COEXISTENCE_MECHANISM	FALSE	Indicate if the preamble-based coexistence mechanism is used by the IUT.





# Annex 2: Protocol Implementation Conformance Statement (PICS)

## PICS related to performance

The device tested is a G3-PLC Cenelec A platform. Testing was performed on phase 1. Operating voltage applied for certification testing was 230V/50Hz .

Name	Value	Unit	Description
<p>PICS related to performance are available through vendor only.</p>			

# Annex 3: Copy of test report cover sheet



**G3-PLC Certification Test Report**

Farlink Technology Limited	ENV-EV8010-QFN56-1PHASE-A2 HW:EV8010 FW: 5046-04
LAN23AF030	Ed.00    October 24, 2023    Page 1/46

## G3-Alliance G3-PLC Platform Certification Test Report

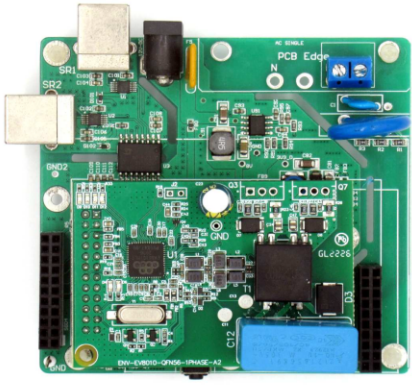
Vendor Name **Farlink Technology Limited**  
 Model Name **ENV-EV8010-QFN56-1PHASE-A2**  
 Serial N° **002**  
 HW version **EV8010**  
 FW version **5046-04**

Test Report # **TR\_LAN23AF030 Ed.00**  
 Date **October 24, 2023**

CONF Tests Specification	<b>version 0.37.</b>	<b>18/01/2022</b>
CONF Tests Suite	<b>version 2.14.</b>	<b>02/2022</b>
IOT Tests Specification	<b>version 0.14.</b>	<b>11/11/2022</b>
IOT Tests Suite	<b>version 2.7.</b>	<b>11/2021</b>
PERF Tests Specification	<b>version 0.27.</b>	<b>05/03/2019</b>
PERF Tests Suite	<b>version 2.14.</b>	<b>02/2022</b>

Test Tool **version 3.2**  
 Tester Modem **version 2.0.1**  
 Certification Test Procedures **version 6.04**    **06/09/2022**

Certification Profile **A (CENELEC A)**  
 Role **PAN Device**  
 Overall Verdict **PASS**



Initiation	Date	Description of modification	Ed.
Omar DIOUF	October 24, 2023	Creation	00

Name	Realised by	Checked by	Approved by
	Omar DIOUF	Vincent BUCHOUX	Thierry DOLIGEZ
Date	October 24, 2023	October 24, 2023	October 24, 2023

Sign			
			

The current report and the test results produced in this current are given for information only and must not be relied on by any third person for any reason.  
 This report contains an assessment of the apparatus carried out on samples submitted to the laboratory. The results in this report relate only to the items tested and were obtained in the period between the initial receipt of samples and the issue of the report. It should be noted that technical hardware or software modifications on the apparatus may impact the results reported in this document.



## Annex 4: Additional details of the certified platform

<b>Platform model name:</b>	ENV-EV8010-QFN56-1PHASE-A2
<b>Platform hardware version:</b>	EV8010
<b>Platform firmware version:</b>	5046-04
<b>Exact part number of all the chips running G3 stack in the certified platform:</b>	EV8010IMLTRT
<b>What each part number runs: lower MAC (incl. CSMA/CA) or PHY or other parts of the stack:</b>	PHY,MAC and 6LowPAN
<b>Hardware version of this chip:</b>	EV8010
<b>Software version running on this chip:</b>	5
<b>Internal CPU frequency:</b>	240 MHz